

# Reversing a firmware uploader

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# **Others NFC stories**

#### About me

- My name's Slurdge/Aurélien
- I'm an enthusiast about NFC, not an expert! My main work involves games
- I know a bit of reverse engineering
- Let's see how to combine them
- I like to have open source solutions to handle my hardware.
   Especially embedded hardware where the proprietary tools are mainly Windows based with cumbersome licenses.

#### Plan

- 1. A short story
- 2. Reversing the Chameleon-Mini clone uploader
- 3. The strange case of the half working NFC tag

# A short story

- I once put my hand on a LF tag of a friend. Excited, I tried to clone with my brand new proxmark.
  - I was a bit too ambitious and erased the tag (it accepted any write command). Now, there was a **backup** tag in his car, which was in this garage.
- The garage could be opened only by a valid tag.
- And it was a Sunday night.
- Lesson learned: be **very careful** with other's people tags! Or you will camp outside a garage waiting for someone to come in

#### **Chameleon-Mini**

" The ChameleonMini is a versatile contactless smartcard emulator compliant to NFC. To support our project, buy it here: <u>https://shop.kasper.it</u>.

It was created by David Oswald and Timo Kasper.

The original ChameleonMini is now at revision G. ChameleonMini is open, you can find the whole hardware and firmware files at <a href="https://github.com/emsec/ChameleonMini">https://github.com/emsec/ChameleonMini</a>.

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"

#### **Chameleon Mini: RevE Rebooted**

- " European Exclusive to Lab401, the Chameleon Mini: RevE Rebooted is a highly optimized fork of the original project.
- Project was done by ProxGrind (hardware) and dxls (firmware) for Lab401
- It was later open sourced
- You can find it pretty easily on *certain* Chinese websites.

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#### How to upload the Firmware

If you browse the product page on AliExpress, this message is written in big green letters:

" Big News: we decide to make the reboot open source, so, after you place order, will give you the link.

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# How to upload the Firmware

Good. Let's ask them then 🔁

Oh, it was **\***iceman's repository all along... **2** 

Let's download this Google Drive package anyway.

*<i>iceman of proxmark fame* 

Lily Zhang: all software here:

https://drive.google.com/file/d/0B1JMWID3C7KNdWMyZXc3WXFROTQ/view

we join githup, opensource now! https://github.com/iceman1001/ChameleonMini-rebooted

How to flash on windows: https://github.com/iceman1001/ChameleonMini-rebooted/wiki/Flashing---windows

Chameleon mini rev rebooted - firmware https://github.com/iceman1001/ChameleonMini-rebooted

Chameleon mini rev rebooted - GUI https://github.com/iceman1001/ChameleonMini-rebootedGUI



We want to:

- Build a firmware
- Upload a firmware
- Get rid of those pesky executable files
- Work from Windows, Linux, MacOS
- Should be an interesting challenge!

#### What we have so far

Two executables

BOOT\_LOADER\_EXE.exe

Createbin.exe

Yeah, can't get much more generic than that... Except maybe BOOT\_LOADER\_EXE\_DOT\_EXE

It's pretty obvious that Createbin.exe is responsible to create the file used by BOOT\_LOADER\_EXE.exe, since it's written in the github wiki.

#### What we have so far

There are also issues on <u>https://github.com/iceman1001/ChameleonMini-rebooted/</u> that are talking about some AES encryption, file manipulation and so on...

So it may simply be a case of finding the AES key! Should be pretty easy to find in the executable... However that would mean there is an AES engine in the bootloader?

Strange (as the chip is not that powerful)... Let's investigate it later!

#### **Running** BOOT\_LOADER\_EXE.exe

If we use the **BOOT\_LOADER\_EXE.exe** file on Windows, after putting the Chameleon-Mini rebooted in DFU mode, we get this:

Our first step is to duplicate the Createbin.exe so we can create binary files that would be accepted by the uploader part. Let's fire... Ghidra

It's simply a matter of opening the exe in Ghidra and it decompiles itself nicely. It's very easy to follow the flow and find the main function. Only slight editing has been done.

*PTS2019 note: I did again the reverse engineering with Ghidra to show how to do it with open source tools.* 

| Menu 🗧 | 🤨 📄 诌 🛛 🛷 Ghidra: bootl  | 🦸 CodeBrowser 🖸 Scrij                               | ot Manag  |                                      |   |
|--------|--|---|---|--------------------------------------|---|
|        |  | A C + B D G   |   |                                      |   |
| B O    |  |   |   |                                      |   |
|        |  |   | CodeBrowser: bootloade                            | er:/BOOT_LOADER_EXE.exe              | - 🗴 😣   |
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|        |  | Listing: BOOT_LOADER_EXE.exe                        |   | s 🖍   도   🎟 14   🖄   💷 -             | × Procompile: atmel_set_fuse - (B ⑤   ►   2   1 = ×   |
|        | ▼ DOOT_LOADER_EXE.exe BOOT_LOADER_EXE.exe Headers                                    | BOOT_LOADER_EXE.exe X                               |   |                                      | voidfastcall atmel_set_fuse(int *param_1,undefined part   |
|        | .text 🗃 .rdata   | 0040289f 5b<br>004028a0 e8 14 5c                    | POP EBX<br>CALL FUN_004084b9                      | undefined FUN_004084b9(u             | <b>-</b> 4 <b>{</b>   |
|        | 🛃 .data  | 00 00<br>004028a5 8b e5                             | MOV ESP, EBP                                      |                                      | 5 char cVar1;<br>6 FILE *pFVar2;  |
|        | irsrc<br>.reloc  | 004028a7 5d<br>004028a8 c3                          | POP EBP<br>RET                                    | 1                                    | 7     int iVar3;       8     byte bVar4;  |
|        | Debug Data   | 004028a9 cc   | ?? CCh  | •                                    | 9 int iVar5;<br>10 ushort *puVar6;  |
|        |  | 004028aa cc<br>004028ab cc                          | ?? CCh<br>?? CCh                                  |                                      | 11 char *_Format;   |
|        | Program Tree ×   | 004028ac cc<br>004028ad cc                          | ?? CCh<br>?? CCh                                  |                                      | 12       undefined local_4c [8];         13       int local_44;   |
|        | riogram nee  | 004028ae cc<br>004028af cc                          | ?? CCh<br>?? CCh                                  |                                      | <pre>14 int local_40;<br/>15 ushort *local_2c;</pre>  |
|        | 🚠 Symbol Tree 🛛 📓 🎽 🗡  |   | *****   |                                      | 16     ushort local_28;       17     ushort local_26;   |
|        | J → f atmel_select_page ^  |   | * FUNCTION  | *                                    | 18 ushort local_24;   |
|        | → f atmel_set_fuse   |   | <pre>www.www.www.www.www.www.www.www.www.ww</pre> |                                      | 19 uint local_8;  |
|        | F atmel validate buffi   | undefined<br>int *                                  | AL:1 <return><br/>ECX:4 param 1</return>          |                                      | <pre>21 local_8 = DAT_0040e000 ^ (uint)&amp;stack0xfffffffc<br/>22 if (param_1 == (int *)0x0) {</pre>                           |
|        | ▶ \$ commands.c  | undefined<br>ushort                                 | DL:1 param_2<br>Stack[0x4]:2 param 3              | XREF[3]: 00402980(R),                | <pre>23 FUN_00408470("atmel.c","atmel_set_fuse",0x20d,0x32,"<br/>24 FUN_004084b9();</pre>                                       |
| , R    | וֹם אָל dfu.c  |   | states (over 112 param_s                          | 004029ba(R),<br>00402afa(R)          | 25 return;<br>26 }  |
|        | → f dfu_abort  | undefined4  | Stack[-0x8]:4 local_8                             | XREF[6]: 004028bd(W),                | 27 if ((*(byte *)(param_1 + 2) & 4) == 0) {   |
|        | Filter:  |   |   | 004028ed(R),<br>0040293c(R),         | <pre>28 FUN_00408470("atmel.c","atmel_set_fuse",0x212,0x32,"1 29 _Format = "target does not support fuse operation.\n" 20</pre> |
|        |  |   |   |                                      | 30 goto LAB_00402924;<br>31 }   |
|        | 🖻 🖻 Data Type Manager 🛛 👻 🗶  | undefined2  | Stack[-0x24]:2local 24                            | 00402b71(R)<br>XREF[1]: 004029d4(W)  | <pre>32 iVar3 = atmel_select_memory_unit(param_1,3);<br/>33 if (iVar3 != 0) {</pre>   |
|        |  | undefined2  | Stack[-0x24]:2 local_24                           | XREF[2]: 00402995(*),                | 34 FUN_004084b9();  |
|        | <ul> <li>✓ ♣ Data Types</li> <li>→ </li> <li>         Ø BuiltInTypes     </li> </ul> | undefined2  | Stack[-0x28]:2 local_28                           | 004029ce(W)<br>XREF[5]: 00402983(*), | 36 }  |
|        | Ø Buildingpes Ø BOOT_LOADER_EXE.ex   |   |   | 0040200£(W)                          | 37 _ switch(naram 2) {  |
|        | ▶ 💈 windows_vs12_32  | Console - Scripting                                 |   |                                      | 🖻 🗶 🗙   |
|        | 0  | rom: 00402adc To: 00408470 Type: UNCOND<br>0402adc  | _ ·   |                                      |   |
|        | 0  | rom: 00402c52 To: 00408470 Type: UNCONE<br>0402c52  |   |                                      |   |
|        |  | rom: 00402c94 To: 00408470 Type: UNCONE<br>10402c94 | ITIONAL_CALL Op: 0 DEFAULT                        |                                      |   |
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| 2 7    | <b>a</b>   |   |   |                                      | 004028b0 atmel set fuse PUSH FBP  |

004028b0 atmel\_set\_fuse PUSH EBP

**⊥** □ 22:04 ●)

```
void __cdecl main(int argc, char **argv)
  _File = fopen(argv[1], "rb");
  if (_File == (FILE *)0×0) {
    printf("Not find file");
  else {
    fseek(_File,0,2);
    uVar2 = ftell(_File);
    _DstBuf = malloc(-(uint)(0xffffffef < (uint)uVar2) | (uint)uVar2 + 0x10); //rounding
    \_Str = malloc(((uint)uVar2 + 0 \times 10) \times 5);
    if (_DstBuf == (void *)0x0) {
      fclose(_File);
      printf("Not get space");
    else { /*Doing some interesting stuff!*/ }
      //Writing file routines...
      printf("Write done!");
```

```
fread(_DstBuf,(uint)uVar2,1,_File);
local_40 = uVar2;
if ((uVar2 & 0xf) != 0) { //padding
  while (local_40 < uVar2 + (0x10 - ((uint)uVar2 & 0x800000f))) {</pre>
    *(undefined *)((int)_DstBuf + (uint)local_40) = 0;
    local_40 = local_40 + 1;
uVar2 = uVar2 + (0 \times 10 - (uVar2 \& 0 \times f));
iVar3 = thunk_FUN_00414870((uint *)&DAT_00420138,(byte *)"designed by dxls",0x80);
counter.0_2 = 0;
while ((uint)(ushort)counter < (uint)((int)(uint)uVar2 >> 4)) {
  thunk_FUN_00415800(counter * 0x10 + _DstBuf),
                      counter * 0 \times 10 + '-', 0 \times 10;
  aes_operation((uint *)&DAT_00420138,iVar3,
                 (byte *)((uint)(ushort)counter * 0x10 + (int)_DstBuf),
                 (undefined *)((uint)(ushort)counter * 0x10 + (int)_DstBuf));
  counter._0_2_ = (ushort)counter + 1;
```

Did we... did we just find the AES key?

| >>> | len(' | designed | by | dxls") |
|-----|-------|----------|----|--------|
|     |       |          |    |        |

It was that easy! Just need to find the algorithm used. At that point, happy, I wrote a python script that would try all modes of AES and compare the output.

Nothing matches...

Back to the drawing board. We didn't investigate what this function does:

void \_\_cdecl FUN\_00415800(char \*param\_1,char xor\_byte,int size)

```
counter = 0;
while (counter < size) {
  tmp = (xor_byte + counter) ^ param_1[counter];
  param_1[counter] = tmp;
  counter = counter + 1;
}
return;
```

#### So... they single byte xor with a rolling counter... Let's integrate it!

Nothing matches **again** 🔛 💥 ...

I went from happy to sad in a few hours.

We'll have to keep digging deeper. Let's look at thunk\_FUN\_00414870

```
void FUN_00414870(uint *param_1,byte *param_2,int param_3)
 local_18 = thunk_FUN_00414da0(param_1,param_2,param_3);
 local 24 = 0:
  local_30 = local_18 << 2;
 while (local_24 < local_30) {</pre>
   uVar1 = param_1[local_24];
   param_1[local_24] = param_1[local_30];
   param_1[local_30] = uVar1;
   uVar1 = param_1[local_24 + 1];
   param_1[local_24 + 1] = param_1[local_30 + 1];
   param_1[local_30 + 1] = uVar1;
   uVar1 = param_1[local_24 + 2];
   param_1[local_24 + 2] = param_1[local_30 + 2];
   param_1[local_30 + 2] = uVar1;
   local_3c = param_1[local_24 + 3];
   param_1[local_24 + 3] = param_1[local_30 + 3];
   param_1[local_30 + 3] = local_3c;
   local_{24} = local_{24} + 4;
   local_{30} = local_{30} + -4;
```

```
int AES_set_encrypt_key(const unsigned char *userKey, const int bits,
    u32 *rk;
    int i = 0;
    u32 temp;
    if (!userKey || !key)
        return -1;
    if (bits != 128 && bits != 192 && bits != 256)
        return -2;
    rk = key->rd_key;
    if (bits==128)
        key->rounds = 10;
    else if (bits==192)
        key->rounds = 12;
    else
        key->rounds = 14;
```

```
u32 *rk;
int i, j, status;
u32 temp;
/* first, start with an encryption schedule */
status = AES_set_encrypt_key(userKey, bits, key);
if (status < ℓ)
    return status;
rk = key->rd_key;
/* invert the order of the round keys: */
for (i = 0, j = 4*(key->rounds); i < j; i += 4, j -= 4) {</pre>
    temp = rk[i ]; rk[i ] = rk[j ]; rk[j ] = temp;
    temp = rk[i + 1]; rk[i + 1] = rk[j + 1]; rk[j + 1] = temp;
    temp = rk[i + 2]; rk[i + 2] = rk[j + 2]; rk[j + 2] = temp;
    temp = rk[i + 3]; rk[i + 3] = rk[j + 3]; rk[j + 3] = temp;
```

#### The solution

#### It was setting a **decryption** key all along!

The whole program can be rewritten as the following python script:

```
def createbin(file_inp, file_out):
    data_inp = file_inp.read()
    for i in range(0,len(data_inp),16):
        aes = AES.new(b'designed by dxls', AES.MODE_CBC, '\0'*16)
        block = data_inp[i:i+16]
        scrambled = [(block[j] ^ ((0x2d + i + j)&0xff)) for j in range(16)]
        out = aes.decrypt(bytes(scrambled))
        file_out.write(out)
```

#### Moving to BOOT\_LOADER\_EXE.exe

- Use the same way to decompile
- Exe is basically a recompile of dfu-programmer
- The strings are the same, so you can take <u>https://github.com/dfu-programmer/dfu-programmer/blob/master/src/commands.c</u> and rename all functions

#### execute\_flash function

```
iVar2 = intel_hex_to_buffer(local_2c,uVar4,uVar1);
if (iVar2 == 0) {
  iVar2 = FUN_004069d0(*(char **)(param_2 + 0x58),(int)local_2c);
  pcVar5 = fprintf_exref;
 if (-1 < iVar2) {
    pcVar6 = __iob_func_exref;
   if (0 < iVar2) {
      DEBUG("commands.c","execute_flash",0x108,0x28,
            "WARNING: File contains 0x\%X bytes outside target memory.\n");
      if (local_30 == 0) {
        DEBUG("commands.c", "execute_flash", 0x10b, 0x28,
               "There may be data in the user page (offset \#X).\n");
        DEBUG("commands.c", "execute_flash", 0x10c, 0x28, "Inspect the hex file or try flash-user.\n")
```

There is a quite suspicious FUN\_004069d0 function... Pass The Salt 2019

#### The complete solution

FUN\_004069d0 is AES *encryption*. But it does not removes the rolling xor !

In summary

- Createbin xor the buffer and *decrypts* it with AES.
- BOOTLOADER *encrypts* (therefore decrypts it) with AES and uploads it.
- Bootloader undoes the rolling xor

#### The bootloader

We have 3 versions of bootloader compatible with the device:

- atxmega32a4u\_104 : 'Original' ATMEL bootloader
- RevE-atxmega32a4u\_104\_modified.bin : ATMEL bootloader but with correct PINs for buttons

ChameleonMiniRDV2.0\_ATxmega32A4U : Factory driver

#### Inside the bootloader

#### Ghidra is very good, can even decompile AVR code

#### What we can do now

We are able to:

- Build a firmware  $\checkmark$
- Upload a firmware  $\checkmark$
- Get rid of those pesky executable files  $\checkmark$
- Work from Windows, Linux, MacOS  $\checkmark$

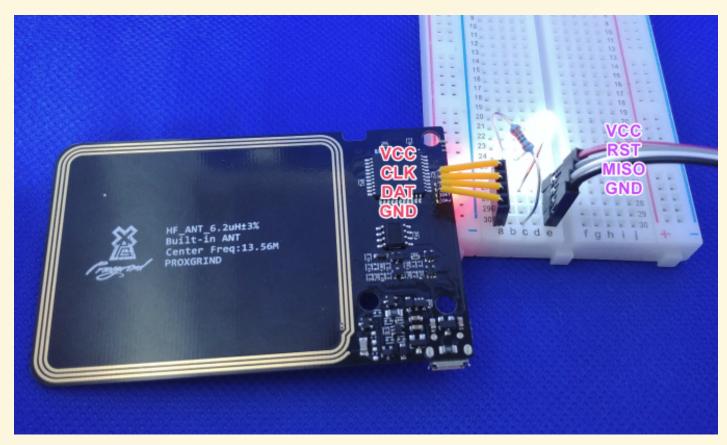
#### How to handle errors

Mistakes happened. While reversing, I tried to upload "prepared" .hex files to the target. However, I would upload a "wrong" file now and then, thus soft bricking my unit 😟.

The only solution is to use a hardware writer with a setup such as an AVRISP mkII.

You can program application data, or, if you reset the whole chip, even reupload a new bootloader.

#### Hardware setup





```
loc = 0x408470 #debug print function
refs = getReferencesTo(toAddr(loc))
for r in refs:
        callee = r.getFromAddress()
        inst = getInstructionAt(callee)
        inst = getInstructionBefore(inst) #C file name push
        inst = getInstructionBefore(inst) #Function name push
        pushaddr = toAddr(inst.getDefaultOperandRepresentation(0))
        if pushaddr > 0x408000: #Simple filter
                func = getFunctionBefore(inst.getAddress())
                if (func.getName().startswith("FUN_")): #Don't rename twice
                        newname = getDataAt(pushaddr).getValue()
                        print(func, "=>", newname)
                        func.setName(newname, ghidra.program.model.symbol.SourceType.USER_DEFINED)
```

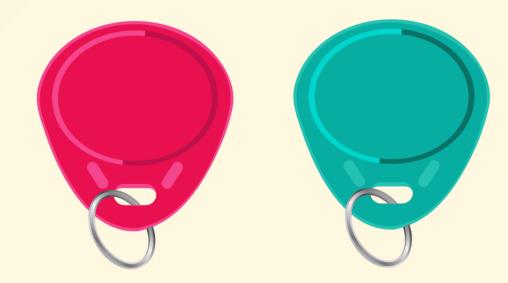
#### Conclusion

- We have a pretty good insight on what's needed in order to create a complete open source solution for "unlocking" the device.
- If you can program the AVR directly, it's quite easy to upload the correct bootloader.
- Next steps
  - Convince a default dfu-programmer to program without verifying (half yes)
  - Upload a program that reprograms the bootloader
  - $\circ~$  See if the SPM helper is present in the original bootloader.

# **Q** The strange case of the half working NFC tags

A little while ago, a friend had an apartment inside a larger structure. He wanted some additional tags so I was happy to make two clones of his two tags.

Let's call them  $red \heartsuit$  and  $green \heartsuit$ .



# Plan of the door locks

There are basically 3 readers:

- One to get to the complex from outside 1 (orange)
- One to get *out* from the complex 2 (cyan)
- One to get to get inside 3 (magenta)

3

# A few month in

Everything works fine and my friend is happy! However, after some months, the clones worked... erratically.

Sometimes the red tag wouldn't work, or green, or both. And I was getting very strange reports.

Some times the door 2 would open, or the door 1, with different tags anytime. I would clone again the tags but the situation would become erratic very quickly.

```
So... what happened?
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```

#### **Diffing to the rescue**

I had the chance to be able to get to the location myself. Armed with proxmark and Mifare Classic Tool, I could finally crack the mystery.

When you approached a tag which was working on reader 1 or 2 (but not 3), it would *write* the tag and it would produce the following diff:

 $AE05FF531F10 \implies AE06FF531F10$ and in another location,  $..0A.... \implies ..09....$ 

#### But why the erratic behaviour

Turns out that the counter wasn't on 8 (or 16...) bits but only on 4 bits and would wrap around. That means, a clone with a wrong counter would work one time out of 16.

So, by pure luck tags would 'work', deactivate the others, etcetera...

Still doesn't explain why the tags would have worked perfectly before

#### **Final take**

By digging in my backups, I found an old dump and where the counter sector is, it was only 0s...

The reader's software must have been upgraded!

But, as with all upgrades, it means you need to have an upgrade plan... So you can craft a 'old' tag with only 0 and it would overwrite the sector and update the counter. But there is a feature of Mifare that specifies access rights 😂

So... let's create a tag with only o in the sector and readonly on that sector. It actually half works! (Exit only)
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# **Thank you/Questions**

If you want to contact me:

- I'm here all 3 days! Come and chat!
- @slurdge on almost any platform/social network
- <u>slurdge@slurdge.org</u>

#### No emojis were harmed during the making of this presentation